AMENDMENTS TO THE DRAWINGS

With regard to Figures 1-5, Applicants submit amended Figures 1-5 to replace originally submitted Figures 1-5. The new Figures 1-5 are submitted herewith.

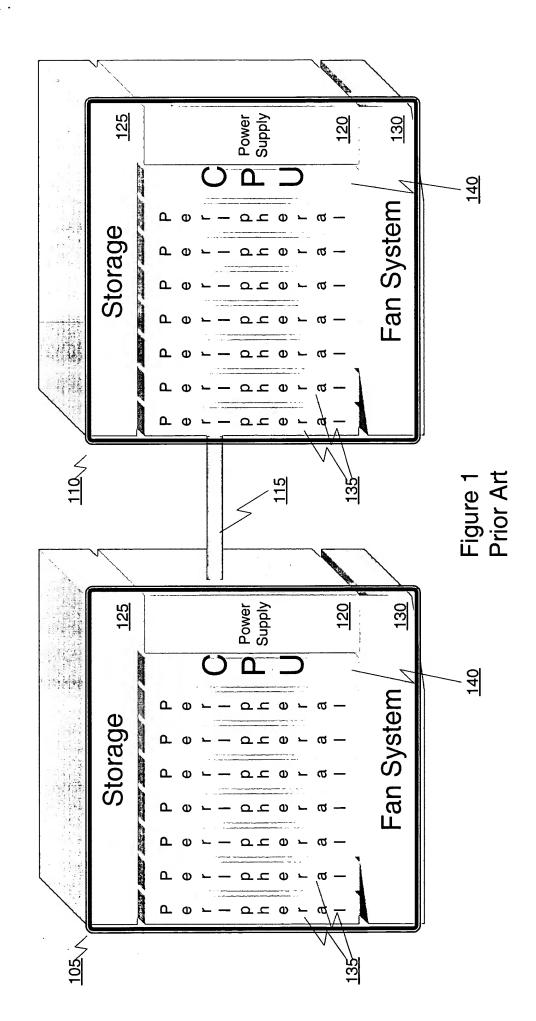
With regard to Figures 6-9, Applicants submit that no changes are required because the Figures are according to one embodiment of the present invention and therefore constitute new art. Applicants respectfully request objections to Figures 6-9 be withdrawn.

With regard to Figures 7, 8, and 10, Applicants submit new and corrected Figures 7, 8, and 10 to replace originally submitted Figures 7, 8, and 10. The new Figures 7, 8, and 10 are submitted herewith.

Docket No.: 42390P12321 Application No.: 09/967,036



Title: "Dual System Masters" Inventors: Clark, et al. Attorney Docket No.: 42390P12321 Application No.: 09/967,036 1/8 REPLACEMENT SHEET





Title: "Dual System Masters" Inventors: Clark, et al. Attorney Docket No.: 42390P12321 Application No.: 09/967,036

REPLACEMENT SHEET

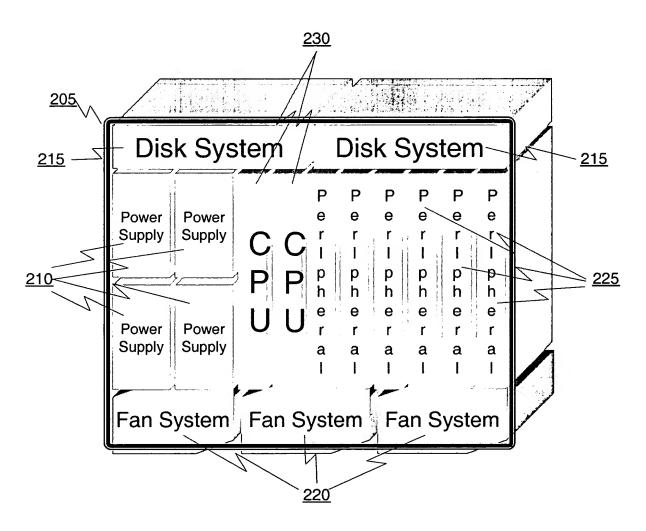


Figure 2 Prior Art

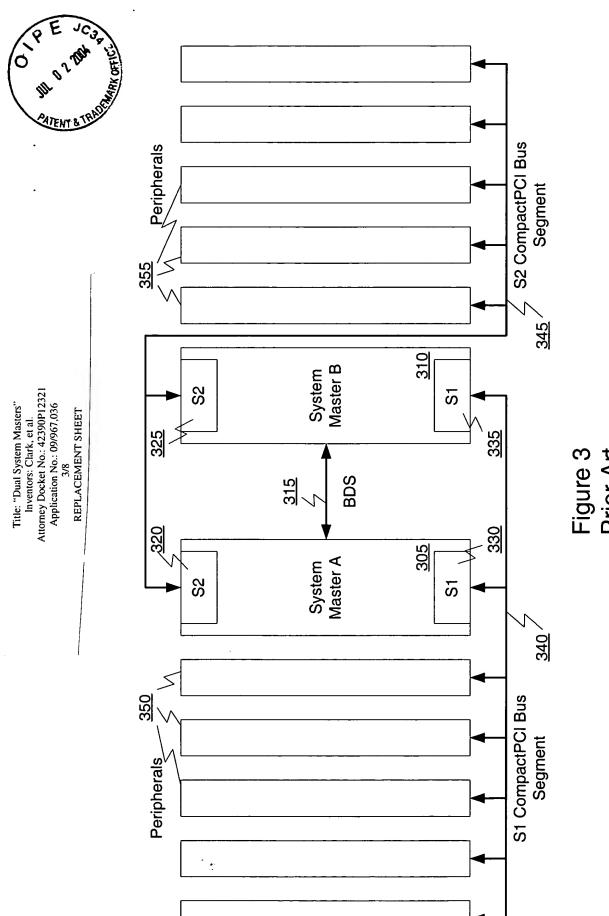


Figure 3 Prior Art



Title: "Dual System Masters" Inventors: Clark, et al. Attorney Docket No.: 42390P12321 Application No.: 09/967,036

REPLACEMENT SHEET

S2 CompactPCI Bus Segment Peripherals 355 345 310 System Master B **S**2 S 325 BDS 315 330 305 System Master A **S**2 S. 340 350 S1 CompactPCI Bus Segment Peripherals

Figure 4 Prior Art



Title: "Dual System Masters" Inventors: Clark, et al. Attorney Docket No.: 42390P12321 Application No.: 09/967,036 5/8 REPLACEMENT SHEET

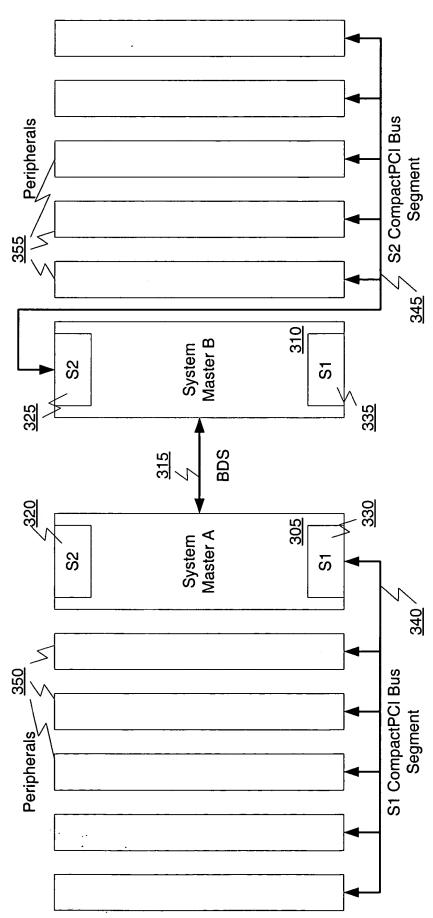


Figure 5 Prior Art

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Title: "Dual System Masters"
Inventors: Clark, et al.
Attorney Docket No.: 42390P12321
Application No.: 09/967,036
6/8
REPLACEMENT SHEET

- 720 - 720 725 - 715 **702** Standby Host RHC COMIM Arbiter Arbiter Clock Clock 100Mbps ENET CompactPCI S2 730-730 **Host Control** CompactPCI S1 <u>750</u> 745 740 730 -730 Clock _ Clock 힏 COMM Arbiter Arbiter Active Host RHC

Figure 7



Title: "Dual System Masters"
Inventors: Clark, et al.
Automey Docket No: 42390P12321
Application No: 09/967,036
7/8
REPLACEMENT SHEET

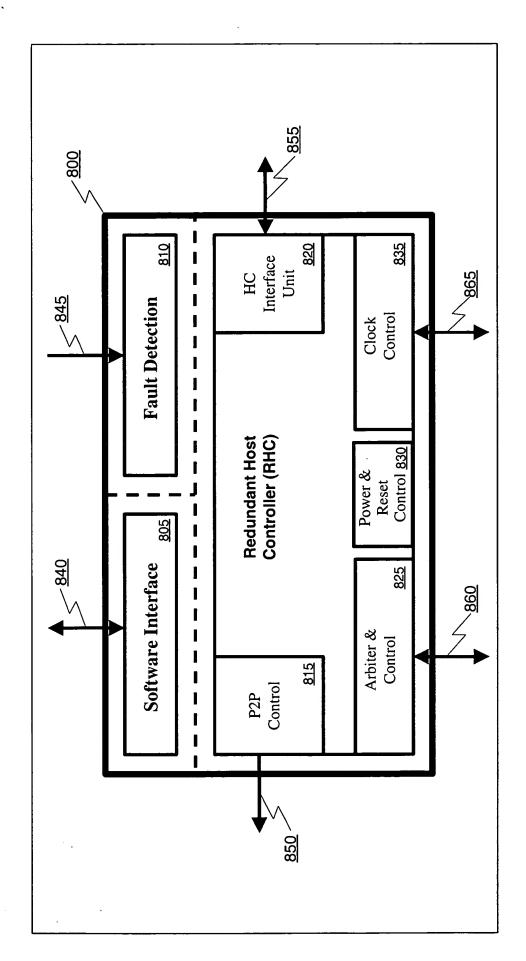


Figure 8



Application No.: 09/967,036 8/8 REPLACEMENT SHEET System boot process 1005 Initialize SM board 1010 1015 this board Perform backup **Backup** active or mode boot backup? process Active 1020 Perform active mode boot process 1025 Perform system host functions End

Figure 10

Title: "Dual System Masters" Inventors: Clark, et al. Attorney Docket No.: 42390P12321